

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L3	11	US-U6452428-\$.DID. OR US-6600339-\$.DID. OR US-6690196-\$.DID. OR US-6707325-\$.DID. OR US-6737893-\$.DID. OR US-6833733-\$.DID. OR US-6870399-\$.DID.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/10 13:25
S1	11	bidirectional adj bridge adj circuit	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/25 15:47
S2	47100	bridge adj circuit	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/08 10:04
S3	2	(bridge adj circuit) same (differential adj amplifier) same asymmetry	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/25 13:36
S4	1888	(bridge adj circuit) same (differential adj amplifier)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/25 13:32
S5	1171	(bridge adj circuit) with (differential adj amplifier)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/25 13:32
S6	371	(bridge adj circuit) and ("375"/\$\$.cls"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/25 13:37
S7	75	(bridge adj circuit) same asymmetry	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/25 13:38
S8	371	(bridge adj circuit) and ("375"/\$\$.ccls"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/25 13:37

S9	66	bidirectional adj bridge	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/25 15:47
S10	1221	bidirectional adj buffer	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/09 11:58
S11	56	bidirectional adj buffer and ("375"/"\$")".ccls"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/28 12:09
S12	2	(bidirectional adj buffer) and (differential adj amplifier) and (gate and mosfet)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/28 12:11
S13	21	(bidirectional adj buffer) and (differential adj amplifier) and (gate and transistor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/28 12:56
S14	1774	(differential adj amplifier) and (logic adj gate) and (transistor or MOS or MOSFET)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/28 12:57
S15	281	(differential adj amplifier) same (logic adj gate) and (transistor or MOS or MOSFET)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/28 12:58
S16	104	(differential adj amplifier) same (logic adj gate) with (transistor or MOS or MOSFET)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/28 13:23
S17	50	buffer and (differential adj amplifier) same (logic adj gate) with (transistor or MOS or MOSFET)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/28 13:30

S18.	9	buffer same (differential adj amplifier) same (logic adj gate) with (transistor or MOS or MOSFET)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/28 13:24
S19	5	(bidirectional adjbuffer) and (differential adj amplifier) same (logic adj gate) with (transistor or MOS or MOSFET)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/28 13:34
S20	0	(bidirectional adj buffer) and (differential adj amplifier) same (logic adj gate) with (transistor or MOS or MOSFET)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/28 13:31
S21	39	(bidirectional adjbuffer) and (differential adj amplifier) and (logic adj gate) with (transistor or MOS or MOSFET)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/28 13:35
S22	1	(bidirectional adj buffer) and (differential adj amplifier) and (logic adj gate) with (transistor or MOS or MOSFET)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/28 13:59
S23	224	(driver) and (differential adj amplifier) and (logic adj gate) with (transistor or MOS or MOSFET)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/28 14:12
S24	312	(driver or bridge or buffer) and (differential adj amplifier) and (logic adj gate) with (transistor or MOS or MOSFET)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/28 16:05
S25	14155	(driver) and (transmission adj line)(differential adj amplifier) and (logic adj gate) with (transistor or MOS or MOSFET)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/28 14:12
S26	39	(driver) and (transmission adj line) and (differential adj amplifier) and (logic adj gate) with (transistor or MOS or MOSFET)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/28 14:13

S27	31543	(driver or bridge or buffer) and (transmission adj line)(differential adj amplifier) and (logic adj gate) with (transistor or MOS or MOSFET)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/28 16:06
S28	2987	(driver or bridge or buffer) and (transmission adj line and bidirectional)(differential adj amplifier) and (logic adj gate) with (transistor or MOS or MOSFET)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/28 16:06
S29	4	(driver or bridge or buffer) and (transmission adj line and bidirectional) and (differential adj amplifier) and (logic adj gate) with (transistor or MOS or MOSFET)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/28 16:07
S30	4	(driver or bridge or buffer) and (transmission adj line and bidirectional) and (differential adj amplifier) and (logic adj gate) with (transistor or MOS or MOSFET)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/28 16:07
S31	414	(bridge adj circuit) and (bidirectional or duplex\$4) and (integrated adj circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/02 08:40
S32	24	(bridge adj circuit) and (bidirectional or duplex\$4) with (integrated adj circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/02 09:08
S33	58	(bridge adj circuit) and (bidirectional or duplex\$4) and (integrated adj circuit) and (differential adj amplifier) and logic	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/02 09:10
S34	5	(bridge adj circuit) and (bidirectional or duplex\$4) and (integrated adj circuit) and (differential adj amplifier) and logic and (threshold adj voltage)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/02 09:37
S35	294	(bridge adj circuit or bidirectional or duplex\$4) and (integrated adj circuit) and (differential adj amplifier) and logic and (threshold adj voltage)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/02 09:38

S36	288	(bridge adj circuit or bidirectional or duplex\$4) and (integrated adj circuit) and (differential adj amplifier) and logic and (threshold adj voltage) and (transistor or fet or mosfet)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/02 09:39
S37	26	(bridge adj circuit or bidirectional or duplex\$4) and (integrated adj circuit) and (differential adj amplifier) with logic and (threshold adj voltage) and (transistor or fet or mosfet)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/02 09:49
S38	40	(bridge adj circuit or bidirectional or duplex\$4) and (differential adj amplifier) with logic and (threshold adj voltage) and (transistor or fet or mosfet)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/02 09:52
S39	99	(bridge adj circuit or bidirectional or duplex\$4) and (differential adj amplifier) and logic and (threshold adj voltage) and (transistor or fet or mosfet) and (transmission adj line)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/02 09:55
S40	81	(bridge adj circuit or bidirectional or duplex\$4) and (differential adj amplifier) and logic and (threshold adj voltage) and (transistor or fet or mosfet) and (transmission adj line) and buffer	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/02 09:55
S41	81	(bridge adj circuit or bidirectional or duplex\$4) and (differential adj amplifier) and (logic) and (threshold adj voltage) and (transistor or fet or mosfet) and (transmission adj line) and (buffer) and (driver)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/02 10:16
S42	3	(bridge adj circuit or bidirectional or duplex\$4) and (differential adj amplifier) and (logic) and (threshold adj voltage) and (transistor or fet or mosfet) and (transmission adj line) and (buffer) and (driver) and repeater	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/02 10:26
S43	3	(bridge adj circuit or bidirectional or duplex\$4) and (differential adj amplifier) and (logic) and (threshold adj voltage) and (transistor or fet or mosfet) and (transmission adj line) and (buffer) and repeater	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/02 10:26
S44	56	(bridge adj circuit or bidirectional or duplex\$4) and (differential adj amplifier) and (logic) and (threshold adj voltage) and (transistor or fet or mosfet) and (transmission adj line) and (buffer) and cable	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/02 10:31

S45	203	(differential adj amplifier) and (common adj mode) and (logic same gate) and (transmission adj line)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/02 10:33
S46	61	(differential adj amplifier) and (common adj mode) and (logic same gate) and (transmission adj line) and bidirectional	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/02 10:35
S47	44	(differential adj amplifier) and (common adj mode) and (logic same gate) and (transmission adj line) and bidirectional and (symmetry or asymmetry)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/02 10:36
S48	5804	(differential adj2 amplifier) with (positive and negative)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/04 09:59
S49	2	(symmetr\$4 with (logic adj2 gate) with mosfet)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/04 10:01
S50	5	(symmetr\$4 with (logic adj2 gate) same mosfet)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/04 10:01
S51	154	symmetr\$4 with (logic adj2 gate)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/04 10:01
S52	2	S48 and S51	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/04 10:01
S53	2	((bridge adj2 circuit\$4) or bidirect\$5 or duplex\$4 or driver\$4) and (differential adj2 amplifier) with (positive and negative) and (symmetr\$4 with (logic adj2 gate))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/04 10:09

S54	2	((bridge adj2 circuit\$4) or bidirect\$5 or duplex\$4 or driver\$4) and (differential adj2 amplifier) with (positive and negative) and (symmetr\$4 same (logic adj2 gate))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/04 10:09
S55	3	((bridge adj2 circuit\$4) or bidirect\$5 or duplex\$4 or driver\$4) and (differential adj2 amplifier) same (positive and negative) and (symmetr\$4 same (logic adj2 gate))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/04 10:09
S56	264	((bridge adj2 circuit\$4) or bidirect\$5 or duplex\$4 or driver\$4) and (differential adj2 amplifier) same (positive and negative) and (logic adj2 gate)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/04 10:10
S57	155	((bridge adj2 circuit\$4) or bidirect\$5 or duplex\$4 or driver\$4) and (differential adj2 amplifier) with (positive and negative) and (logic adj2 gate)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/04 10:10
S58	32	((bridge adj2 circuit\$4) or bidirect\$5 or duplex\$4 or driver\$4) and (differential adj2 amplifier) with (positive and negative) and (logic adj2 gate) and symmetr\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/04 11:24
S59	15	((bridge adj2 circuit\$4) or bidirect\$5 or duplex\$4) and (differential adj2 amplifier) with (positive and negative) and (logic adj2 gate) and symmetr\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/04 11:26
S60	0	((bridge adj2 circuit\$4) or bidirect\$5 or duplex\$4) and (differential adj2 amplifier) with (output adj logic adj signal) and (logic adj2 gate)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/04 11:27
S61	2	((bridge adj2 circuit\$4) or bidirect\$5 or duplex\$4) and (differential adj2 amplifier) with (logic adj signal) and (logic adj2 gate)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/04 11:27
S62	28	((bridge adj2 circuit\$4) or bidirect\$5 or duplex\$4) and (differential adj2 amplifier) with (logic adj signal)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/04 13:50

S66	0	(bidirectional adj2 buffer) and (differential adj amplifier with (output near logic))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/04 13:14
S67	0	(bidirectional adj2 buffer) and (differential adj amplifier same (output near logic))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/04 13:14
S68	8	(bidirectional adj2 buffer) and (differential adj amplifier and (output near logic))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/04 13:14
S69	219	symmetr\$4 with switch\$4 with logic	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/04 13:50
S70	3	(symmetr\$4 with switch\$4 with logic) same (differential adj2 amplifier)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/04 13:56
S71	20	(symmetr\$4 with switch\$4 with logic) and (differential adj2 amplifier)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/04 13:56
S72	47	(time adj division adj duplex) and (differential adj amplifier)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/08 16:28
S73	41	(bidirectional adj buffer) and (differential adj amplifier)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/08 11:18
S74	34	(bidirectional adj buffer) and (differential adj amplifier) and (@ad<="20011120" or @pd<="20011120")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/08 11:20

S75	41	(time adj division adj duplex) and (differential adj amplifier) and (@ad<="20011120" or @pd<="20011120")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/08 11:25
S76	30	(time adj division adj duplex) and (differential adj amplifier) and logic and (@ad<="20011120" or @pd<="20011120")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/08 11:25
S77	2	(time adj division adj duplex) and (differential adj amplifier) and logic and mosfet and (@ad<="20011120" or @pd<="20011120")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/08 11:30
S78	1205	(differential adj amplifier) and logic and mosfet and (@ad<="20011120" or @pd<="20011120")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/08 11:31
S79	179	(differential adj amplifier) and (logic adj gate) and mosfet and (@ad<="20011120" or @pd<="20011120")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/08 11:31
S80	53	(differential adj amplifier) and (logic adj gate) and mosfet and ((positive and negative) with signal) and (@ad<="20011120" or @pd<="20011120")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/08 11:58
S81	31997113	((subtract\$4 differen\$5) with (transmit\$5 outgoing) with (incoming receiv\$4)) (@ad<="20011120" or @pd<="20011120")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/08 12:03
S82	65965	((subtract\$4 differen\$5) with (transmit\$5 outgoing) with (incoming receiv\$4)) and (@ad<="20011120" or @pd<="20011120")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/08 12:04
S83	3572	((subtract\$4) with (transmit\$5 outgoing) with (incoming receiv\$4)) and (@ad<="20011120" or @pd<="20011120")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/08 12:04

S84	3416	((subtract\$4) with (transmit\$5) with (incoming receiv\$4)) and (@ad<="20011120" or @pd<="20011120")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/08 12:04
S85	3358	((subtract\$4) with (transmit\$5) with (receiv\$4)) and (@ad<="20011120" or @pd<="20011120")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/08 12:05
S86	687	((subtract\$4) with (transmit\$5) with (receiv\$4)) and (("375"/\$\$.ccls") and (@ad<="20011120" or @pd<="20011120"))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/08 12:06
S87	23	((subtract\$4) with (transmit\$5) with (receiv\$4)) and tdd and (("375"/\$\$.ccls") and (@ad<="20011120" or @pd<="20011120"))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/08 13:57
S88	21	((subtract\$4) with (transmit\$5) with (receiv\$4)) and (differential adj amplifier) and (("375"/\$\$.ccls") and (@ad<="20011120" or @pd<="20011120"))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/08 13:26
S89	2	((subtract\$4) with (transmit\$5) with (receiv\$4)) same (differential adj amplifier) and (("375"/\$\$.ccls") and (@ad<="20011120" or @pd<="20011120"))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/08 13:33
S90	2347	((375/219).ccls") and (@ad<="20011120" or @pd<="20011120")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/08 13:34
S91	79	S90 and (differential adj amplifier)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/08 13:54
S92	1286	(bridge adj2 circuit) with (differential adj2 amplifier)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/08 13:55

S93	4149	((subtract\$4) with (transmit\$5) with (receiv\$4))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/08 13:58
S94	269	((subtract\$4) with (transmit\$5) with (receiv\$4)) and bidirectional	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/08 15:02
S95	3	((subtract\$4) with (transmit\$5) with (receiv\$4)) and bidirectional adj buffer	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/08 13:58
S96	20	((subtract\$4) with (transmit\$5) with (receiv\$4)) and bidirectional and (differential adj amplifier)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/08 16:45
S97	16	((subtract\$4) with (transmit\$5) with (receiv\$4)) and bidirectional and (differential adj amplifier) and logic	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/08 14:03
S98	5	((subtract\$4) with (transmit\$5) with (receiv\$4)) and bidirectional and (differential adj amplifier) same logic	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/08 15:02
S99	125	((subtract\$4) with (transmit\$5) with (receiv\$4)) and ((bi-directional bidirectional) adj communication)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/08 16:32
S10 0	8	((subtract\$4) with (transmit\$5) with (receiv\$4)) and ((bi-directional bidirectional) adj communication) and (differential adj amplifier)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/08 15:04
S10 1	78	((subtract\$4) with (transmit\$5) with (receiv\$4)) and ((bi-directional bidirectional) adj communication) and (@ad<="20011120" or @pd<="20011120")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/08 16:33

S10 2	10	((subtract\$4) with (transmit\$5) with (receiv\$4)) and bidirectional and (differential adj amplifier) and (@ad<="20011120" or @pd<="20011120")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/08 17:35
S10 3	0	(logic adj2 threshold) with (midsupply mid-supply (mid adj supply)) and (@ad<="20011120" or @pd<="20011120")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/08 17:37
S10 4	1119	(logic adj2 threshold) with (voltage) and (@ad<="20011120" or @pd<="20011120")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/08 17:43
S10 5	280	(threshold adj voltage) with (logic adj gate) and (@ad<="20011120" or @pd<="20011120")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/08 17:48
S10 6	0	(threshold adj voltage) with (logic adj gate) and (mid-supply midsupply (mid adj supply)) and (@ad<="20011120" or @pd<="20011120")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/08 17:44
S10 7	19	(chang\$4 with threshold adj voltage) with (logic adj gate) and (@ad<="20011120" or @pd<="20011120")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/08 17:50
S10 8	2	(controll\$6 with symmetry with transient) and (@ad<="20011120" or @pd<="20011120")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/09 09:28
S10 9	1	(bidirectional and receiver and symmetry with transient) and (@ad<="20011120" or @pd<="20011120")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/09 09:30
S11 0	240	(bidirectional and receiver and symmetry and transient) and (@ad<="20011120" or @pd<="20011120")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/09 09:30

S11 1	173	(bidirectional and receiver and symmetry and transient and threshold) and (@ad<="20011120" or @pd<="20011120")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/09 09:31
S11 2	35	((control\$4 with symmetry) and (switch with transient)) and (@ad<="20011120" or @pd<="20011120")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/09 09:36
S11 3	2	((control\$4 with symmetry) and (switch with transient) and (increase with threshold)) and (@ad<="20011120" or @pd<="20011120")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/09 12:19
S11 4	83	((control\$4 with symmetry) and (switch\$4 with transient)) and (@ad<="20011120" or @pd<="20011120")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/09 09:36
S11 5	18	((control\$4 with symmetry) and (switch\$4 with transient) and receiver) and (@ad<="20011120" or @pd<="20011120")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/09 10:04
S11 6	11	((control\$4 with symmetry) and (switch\$4 adj2 transient) and receiver) and (@ad<="20011120" or @pd<="20011120")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/09 10:05
S11 7	37	((control\$4 with symmetry) and (switch\$4 adj2 transient)) and (@ad<="20011120" or @pd<="20011120")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/09 10:14
S11 8	5	((control\$4 with symmetry) same (switch\$4 adj2 transient)) and (@ad<="20011120" or @pd<="20011120")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/09 10:46
S11 9	54	(differential adj amplifier with asymmetry) and (@ad<="20011120" or @pd<="20011120")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/09 10:47

S12 0	41	(bidirectional adj buffer) and (differential adj amplifier)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/09 11:59
S12 1	34	(bidirectional adj buffer) and (differential adj amplifier) and (@ad<="20011120" or @pd<="20011120")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/09 12:07
S12 2	91	subtract\$4 with (differential adj amplifier) and (@ad<="20011120" or @pd<="20011120")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/09 12:07
S12 3	81	((control\$4 with symmetry) and (increase with threshold)) and (@ad<="20011120" or @pd<="20011120")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/09 12:20
S12 4	4	((control\$4 with symmetry) same (increase with threshold)) and (@ad<="20011120" or @pd<="20011120")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/09 13:59
S12 5	2	"6549971".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/09 13:59
S12 6	2	"5675584".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/09 16:57
S12 7	654	(First adj amplifi\$6) and (second adj amplifi\$6) and (common adj mode) and ((third output) adj amplifi\$6)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/10 13:25
S12 8	28	(First adj amplifi\$6) same (second adj amplifi\$6) same (common adj mode) same ((third output) adj amplifi\$6)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/09 17:01

S12-9	20	(First adj amplifi\$6) same (second adj amplifi\$6) same (common adj mode with amplifi\$6) same ((third output) adj amplifi\$6)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/09 17:17
S130	10	(First adj differential adj amplifi\$6) same (second adj differential adj amplifi\$6) same (common adj mode with amplifi\$6) same ((third output) adj differential adj amplifi\$6)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/10 09:54
S131	2	(First adj amplifi\$6) same (second adj amplifi\$6) same (common adj mode with (differential adj amplifi\$6)) same (output adj differential adj amplifi\$6)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/10 09:55